Fig.1

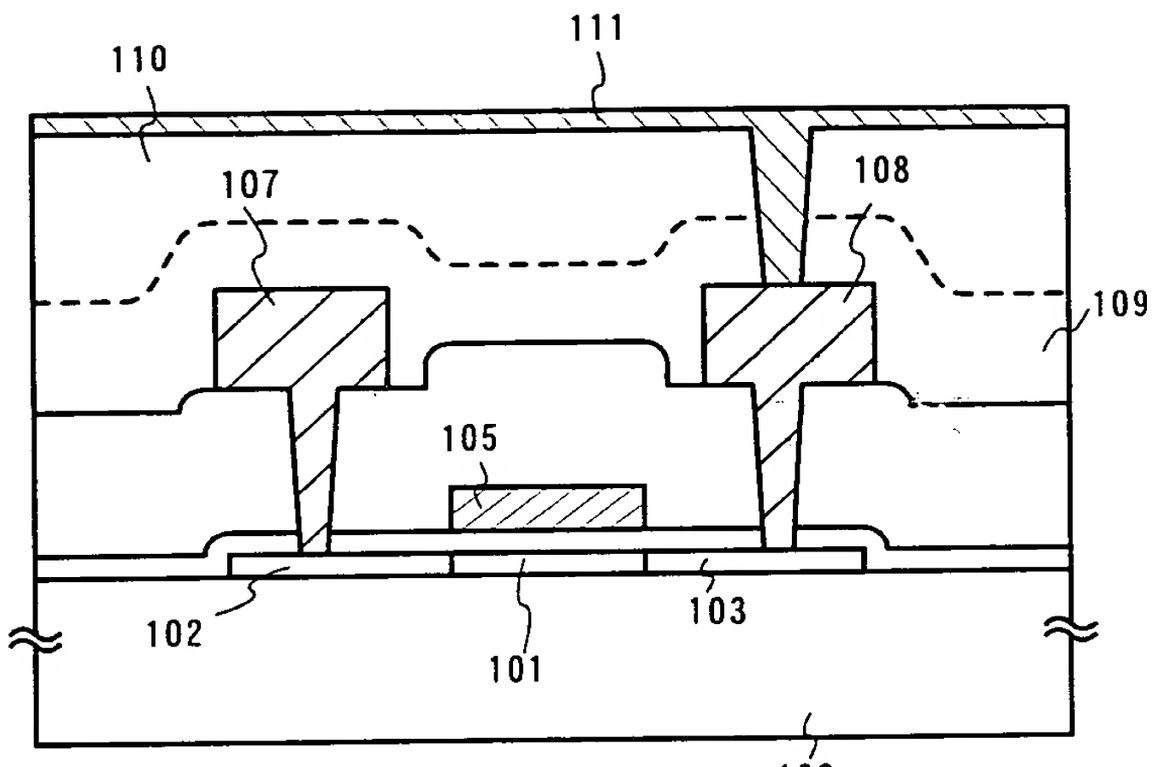


Fig.2

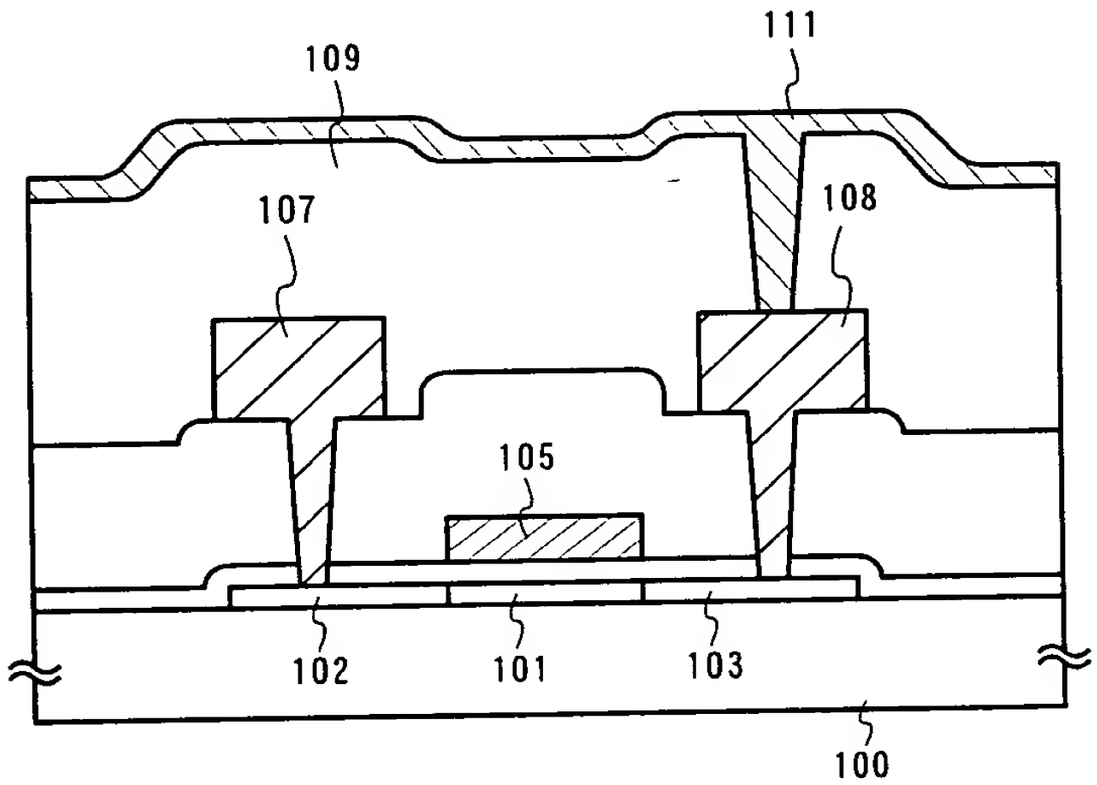


Fig.3

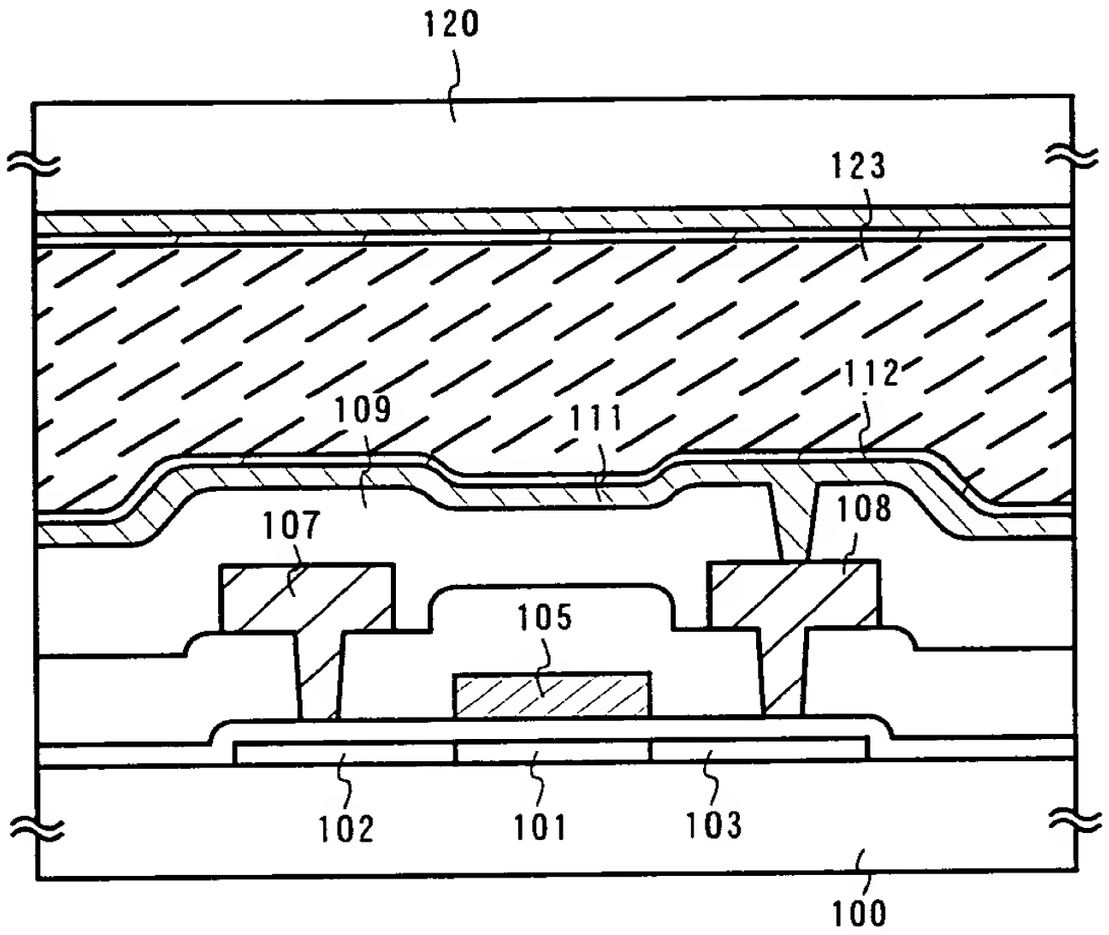


Fig.4

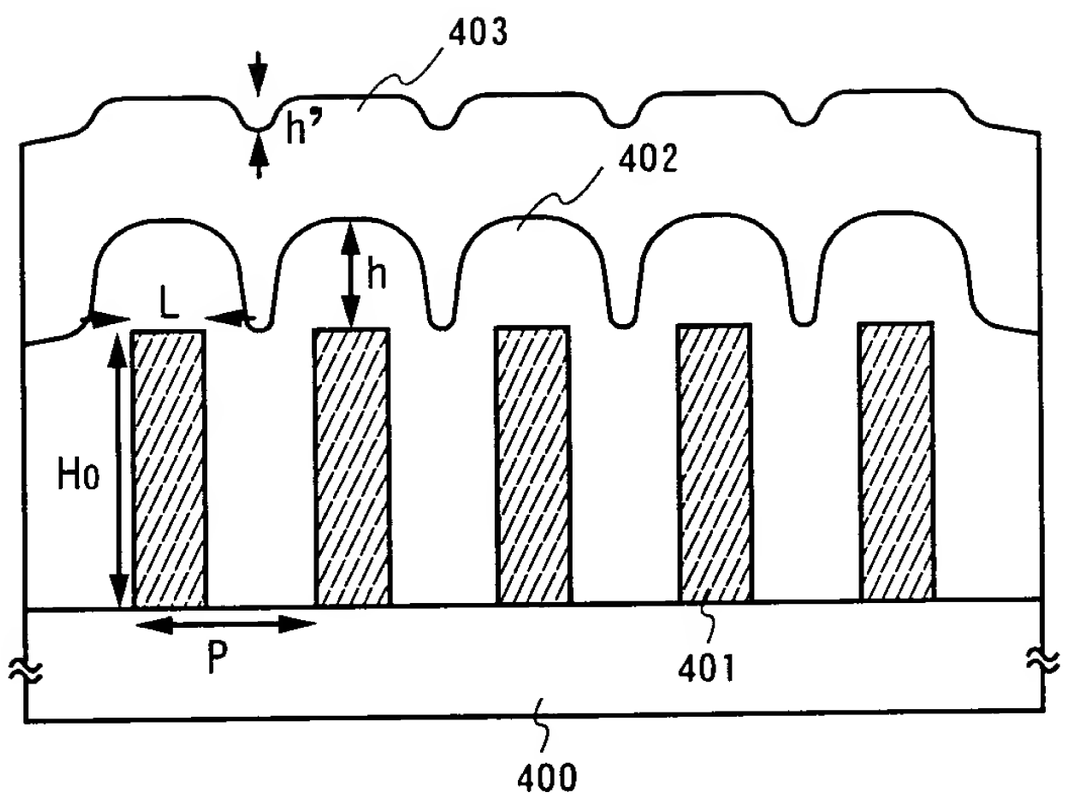


Fig. 5

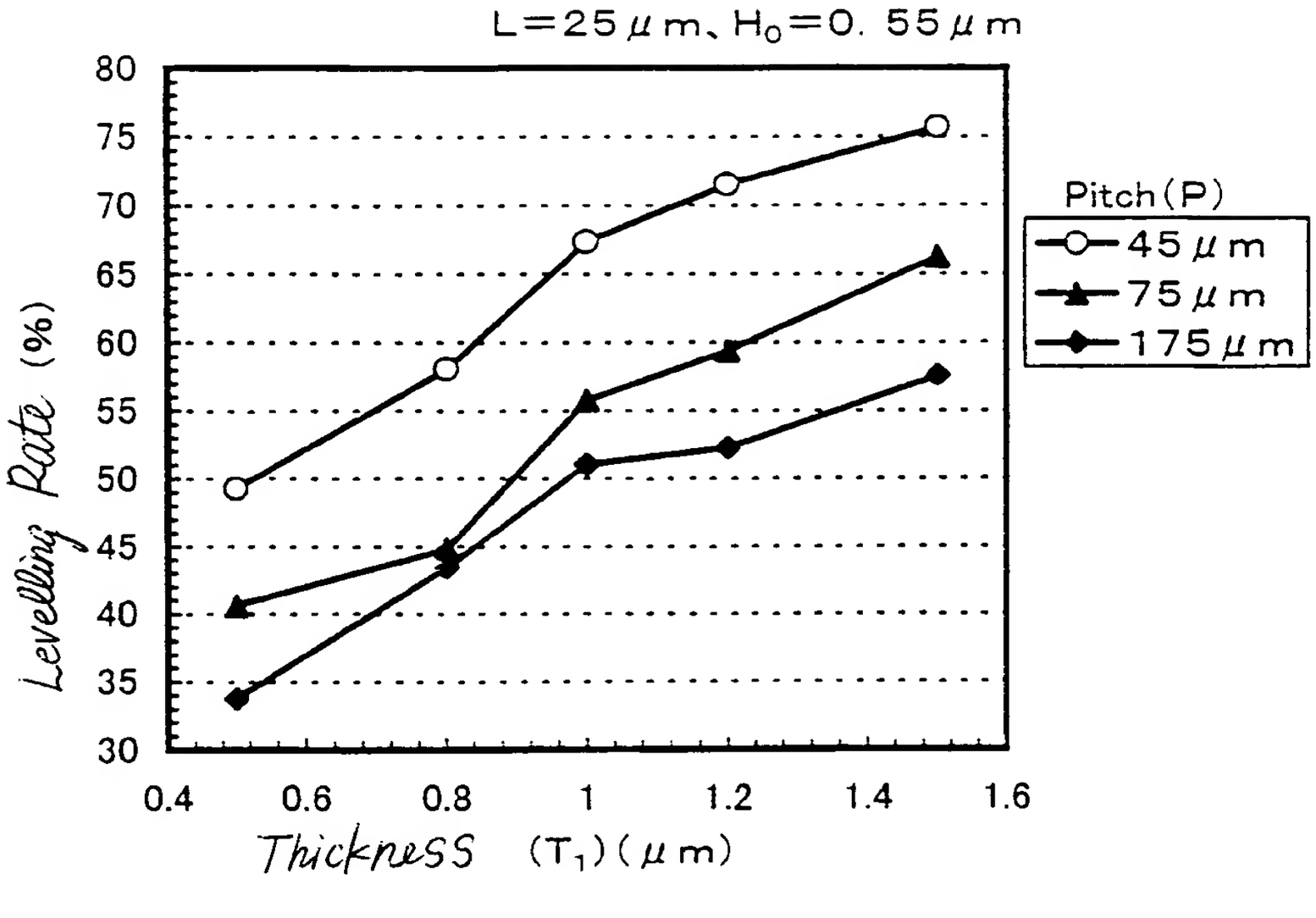

L=25μm、H₀=0.55μm
Levelling Rate (%)
80
75
70
65
60
55
50
45
40
35
30
0.4
0.6
0.8
1
1.2
1.4
1.6
Thickness (T₁)(μm)
Pitch(P)
45μm
75μm
175μm

Fig. 6

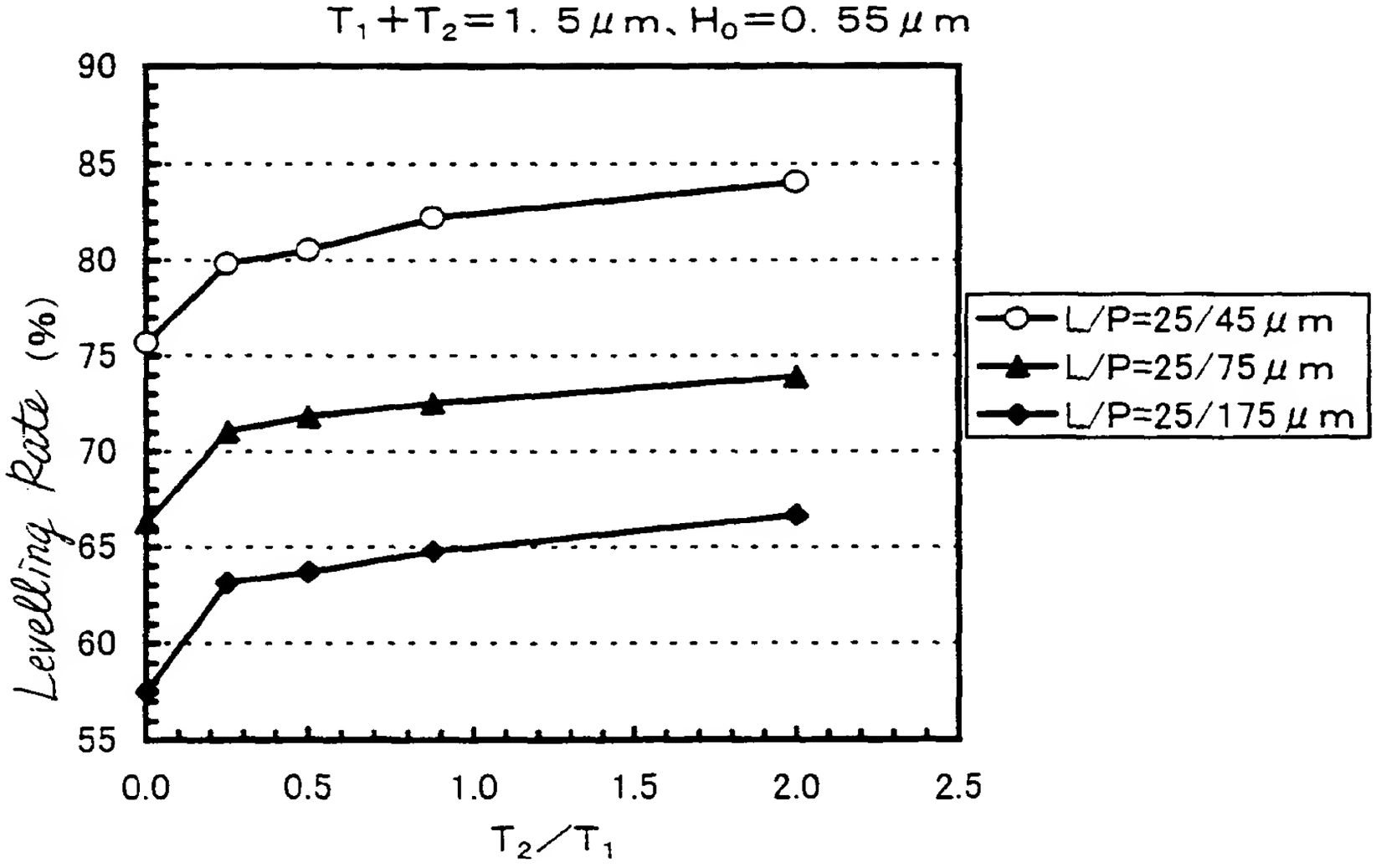

$T_1+T_2=1.5\mu m$、$H_0=0.55\mu m$
90
85
80
75
70
65
60
55
Levelling Rate (%)
0.0
0.5
1.0
1.5
2.0
2.5
T_2/T_1
L/P=25/45 μm
L/P=25/75 μm
L/P=25/175 μm

Fig.7A

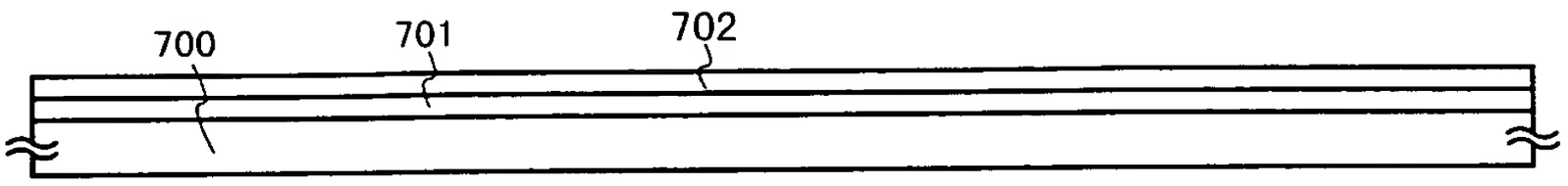


Fig.7B

Fig.7C

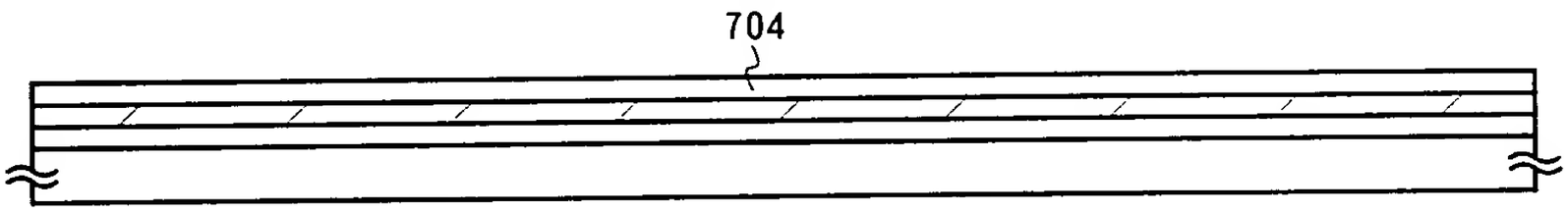


Fig.7D

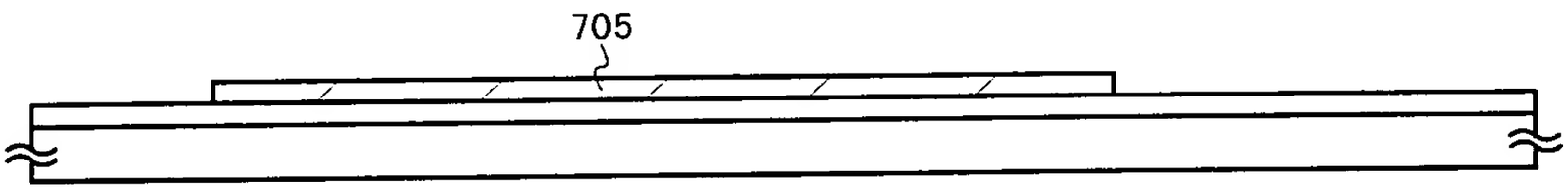
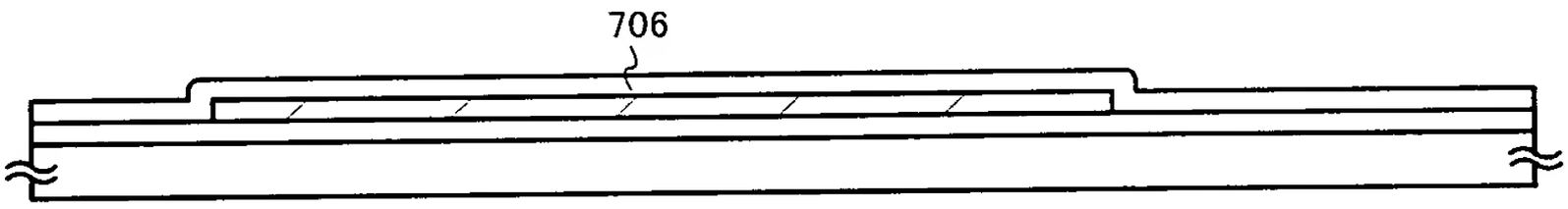


Fig.7E



707

Fig.7F

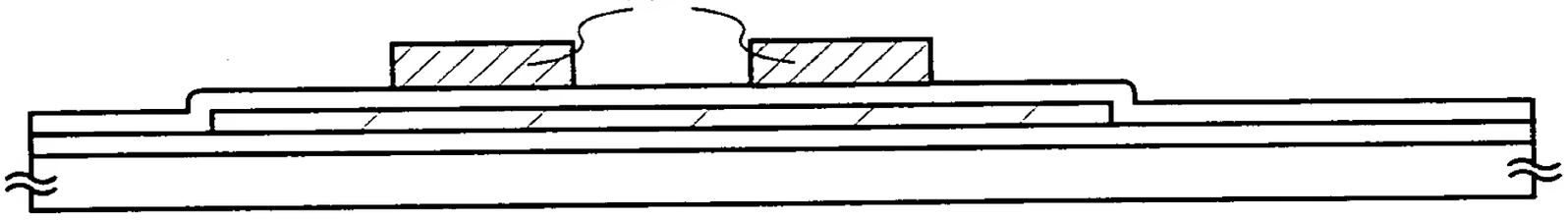
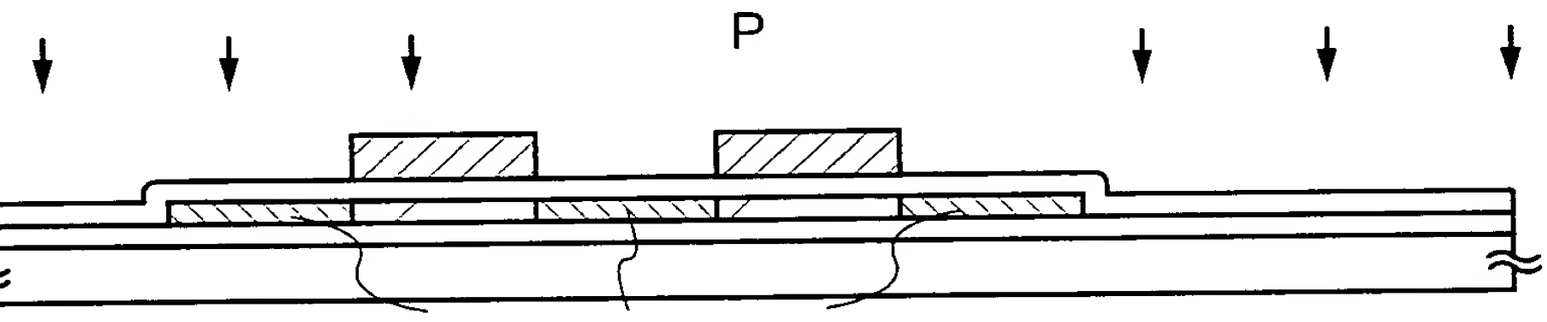


Fig.7G



708

Fig.8A

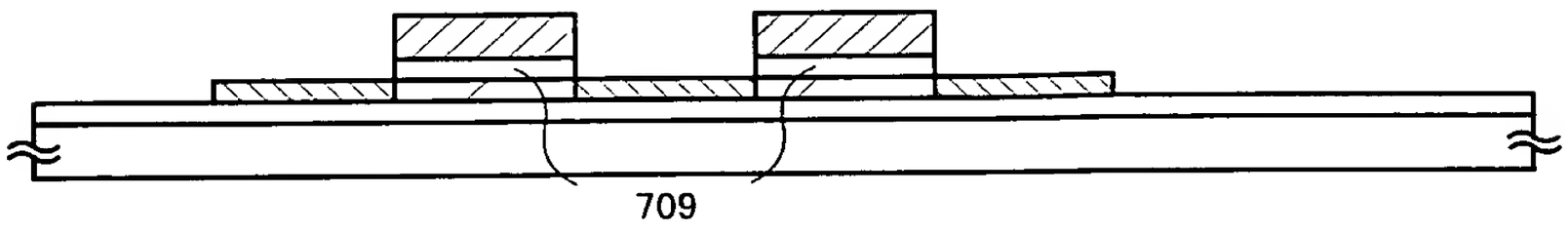


Fig.8B

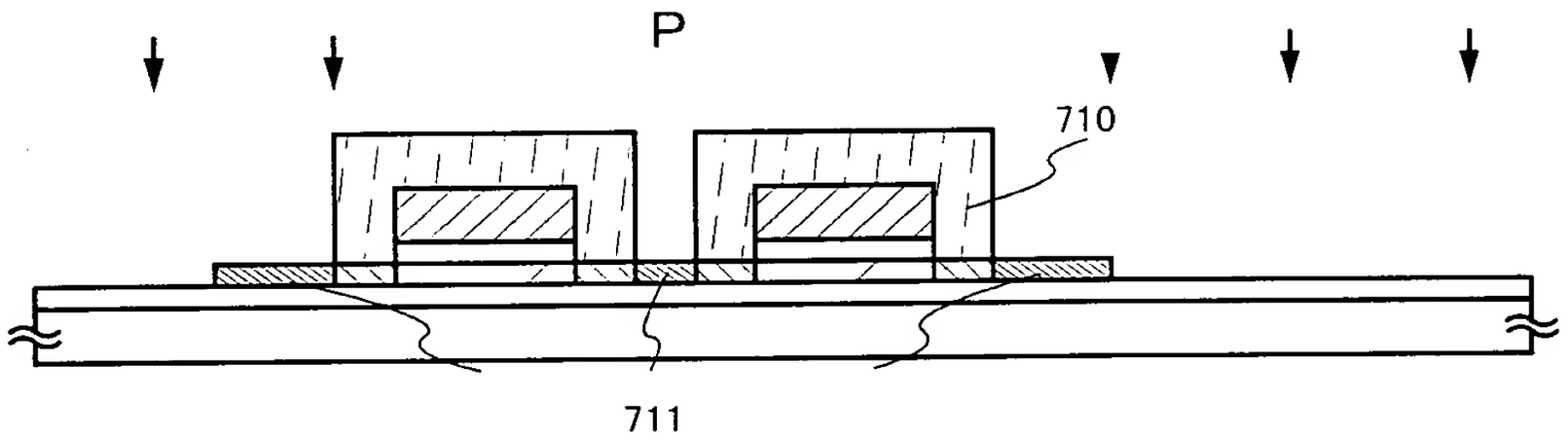


Fig.8C

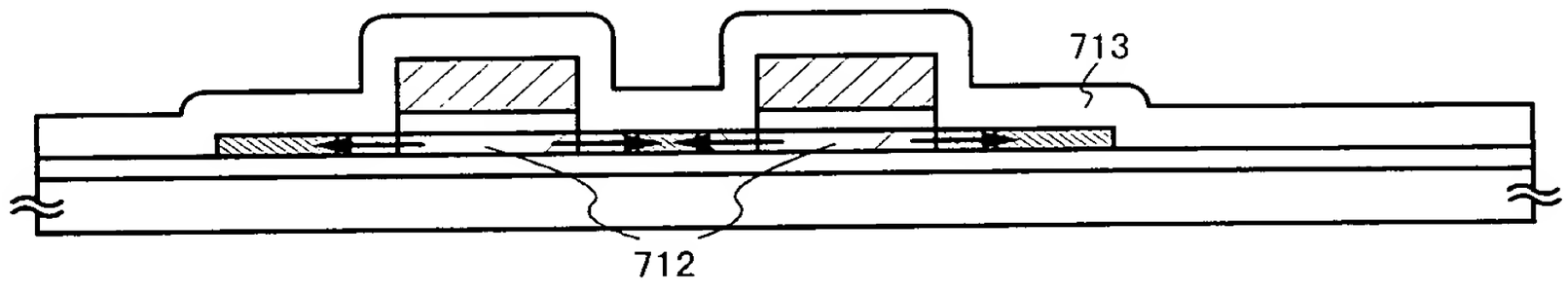


Fig.8D

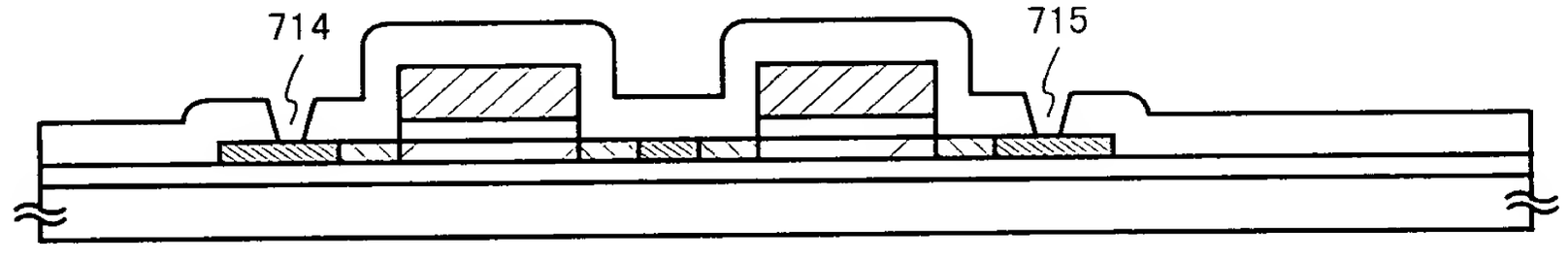
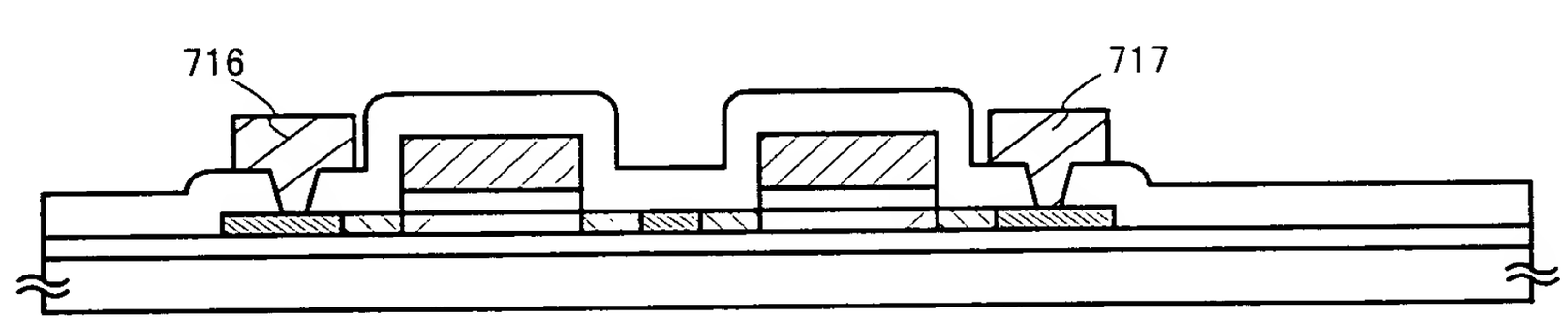


Fig.8E



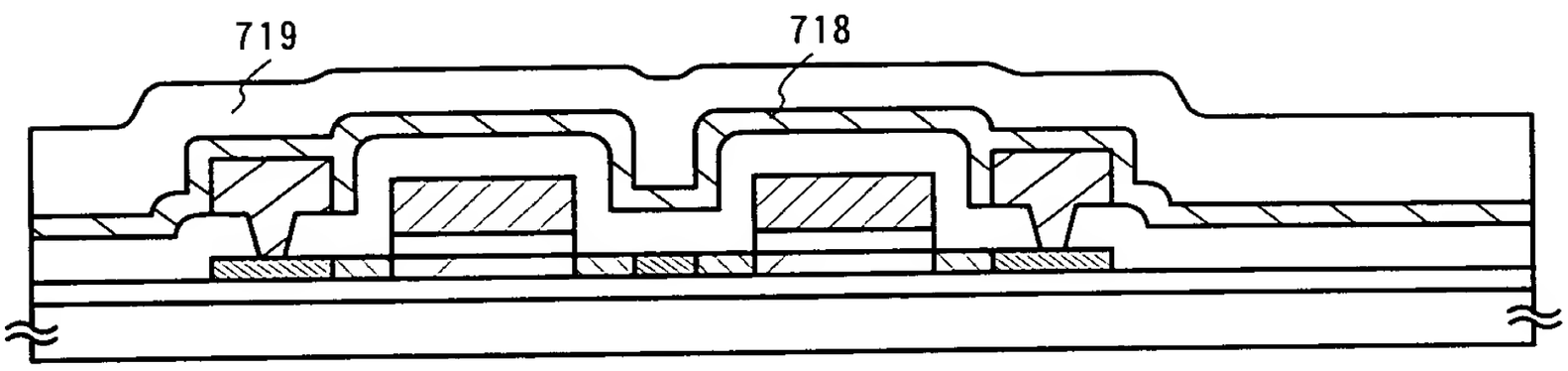


Fig.9A

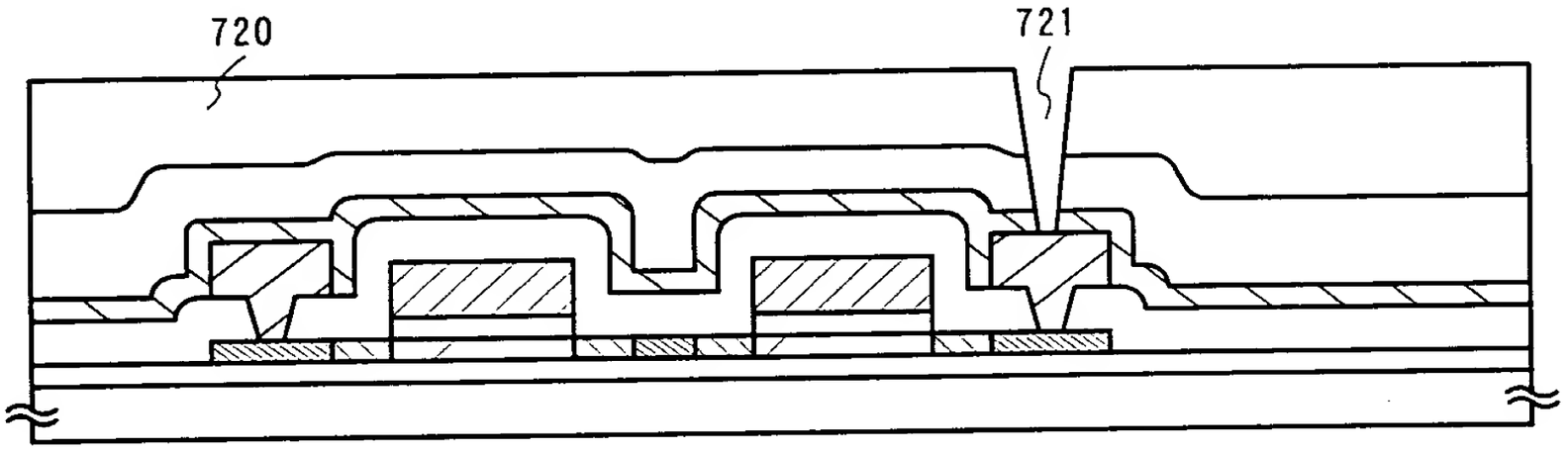


Fig.9B

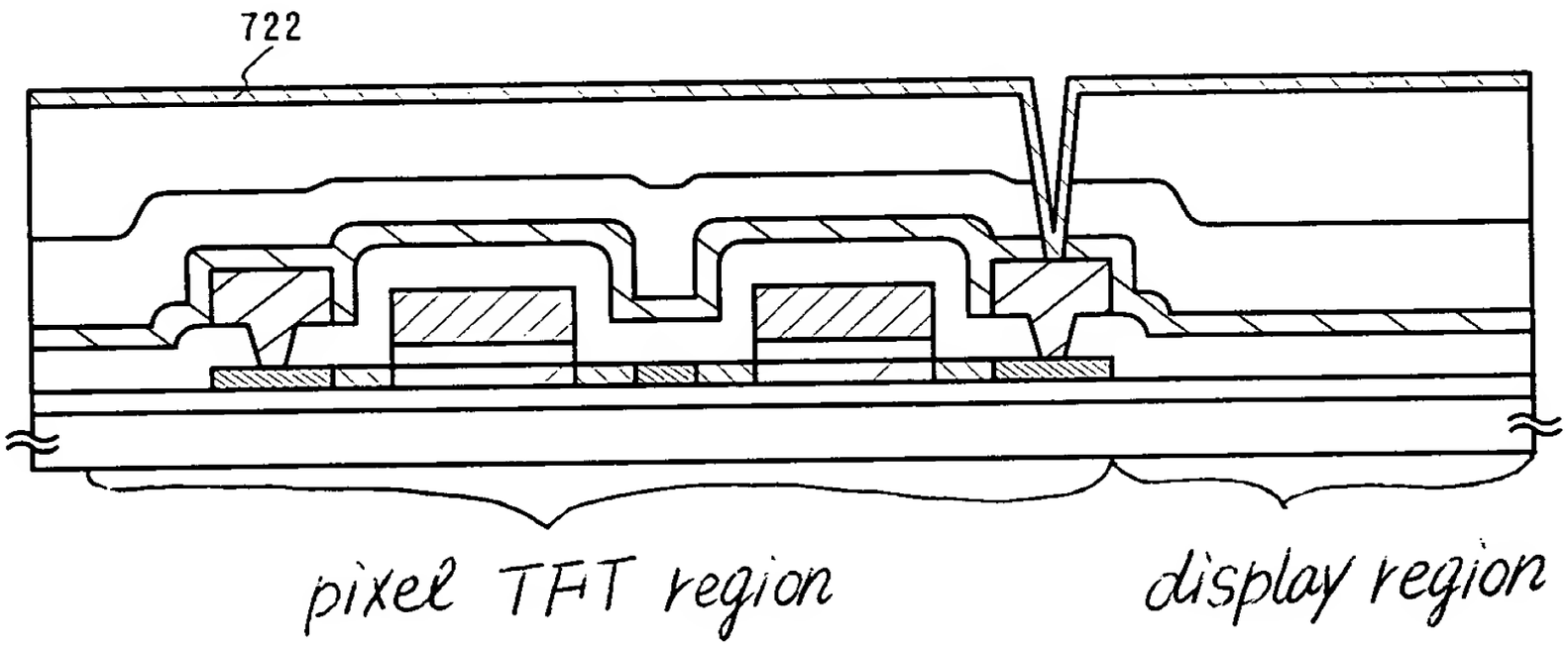


Fig.9C

pixel TFT region display region

Fig.10

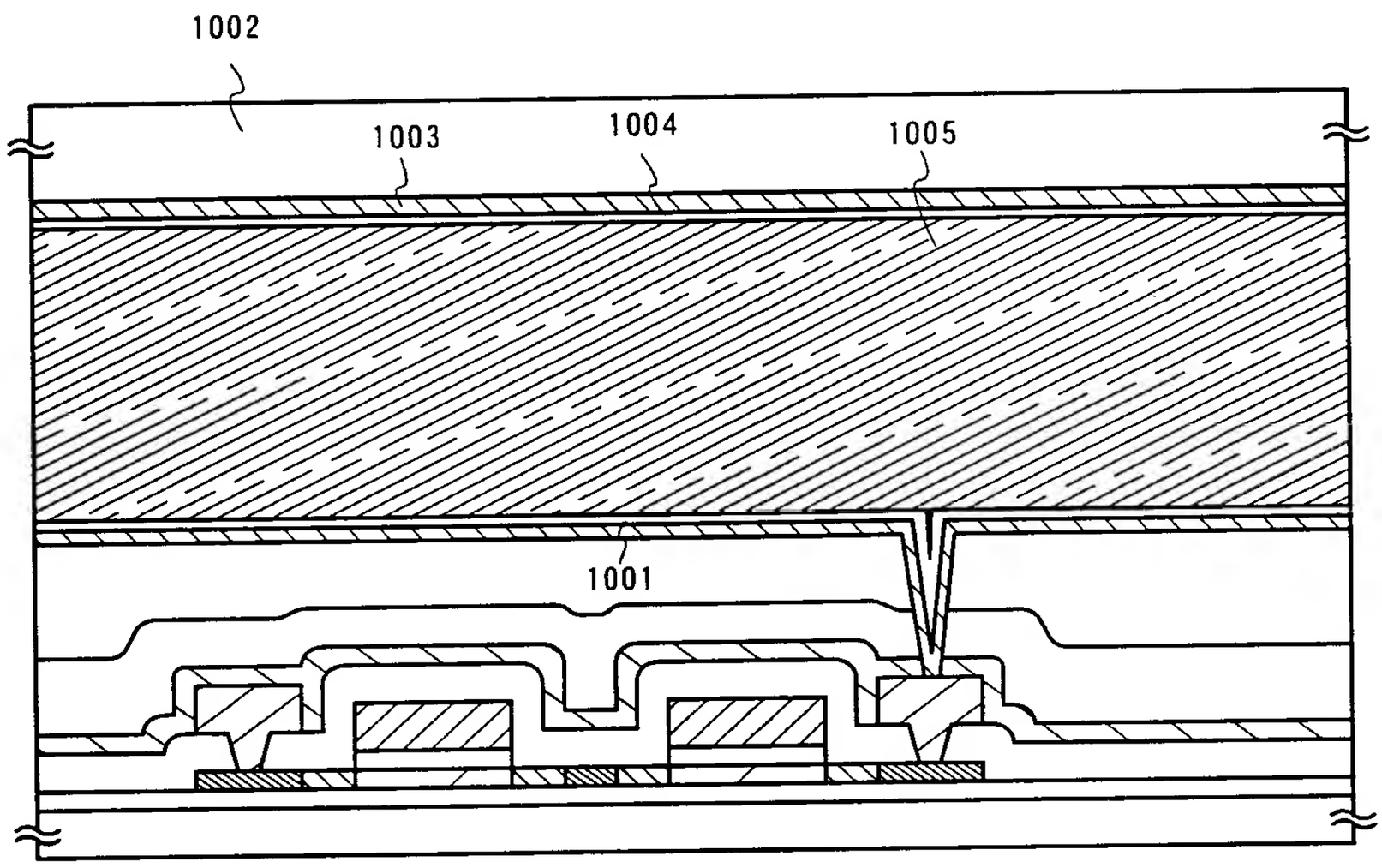


Fig. 11

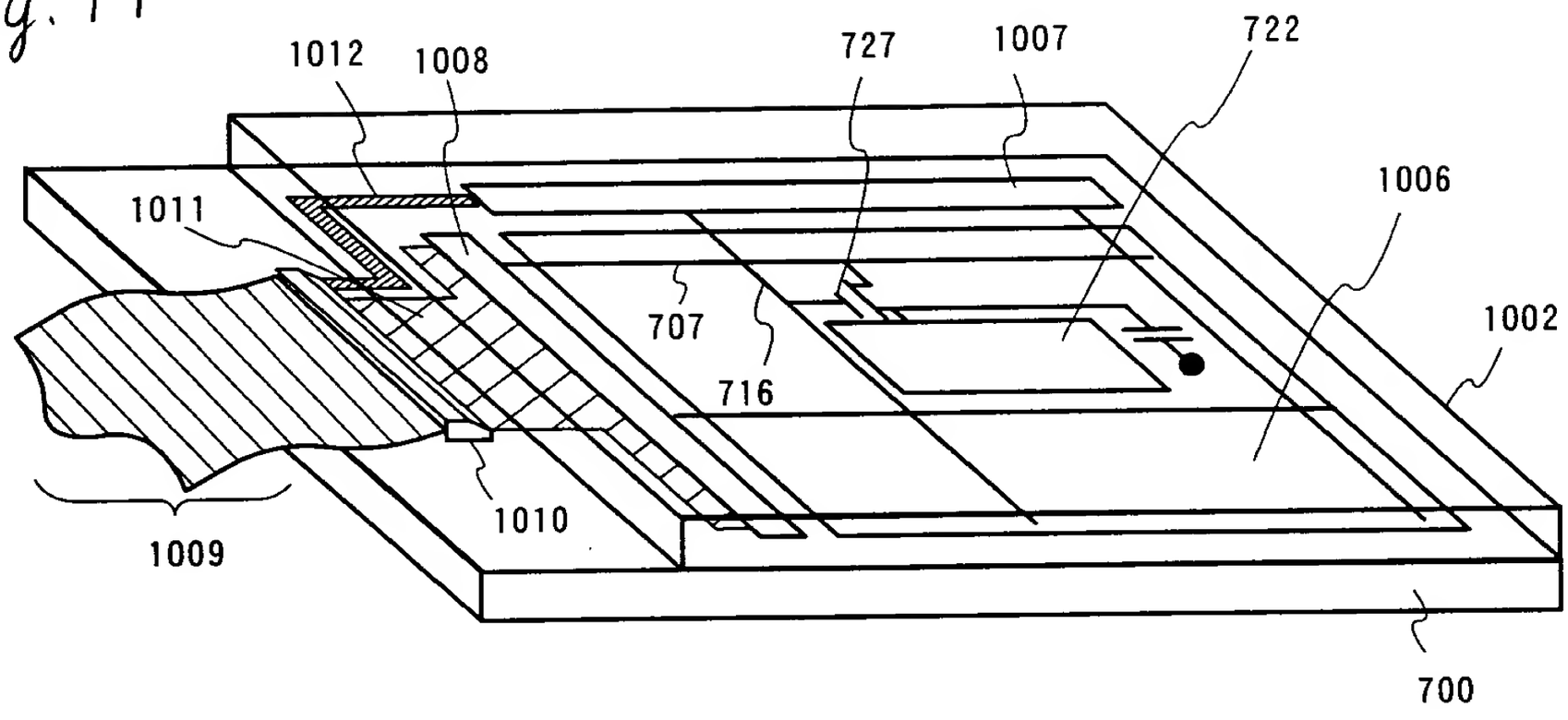
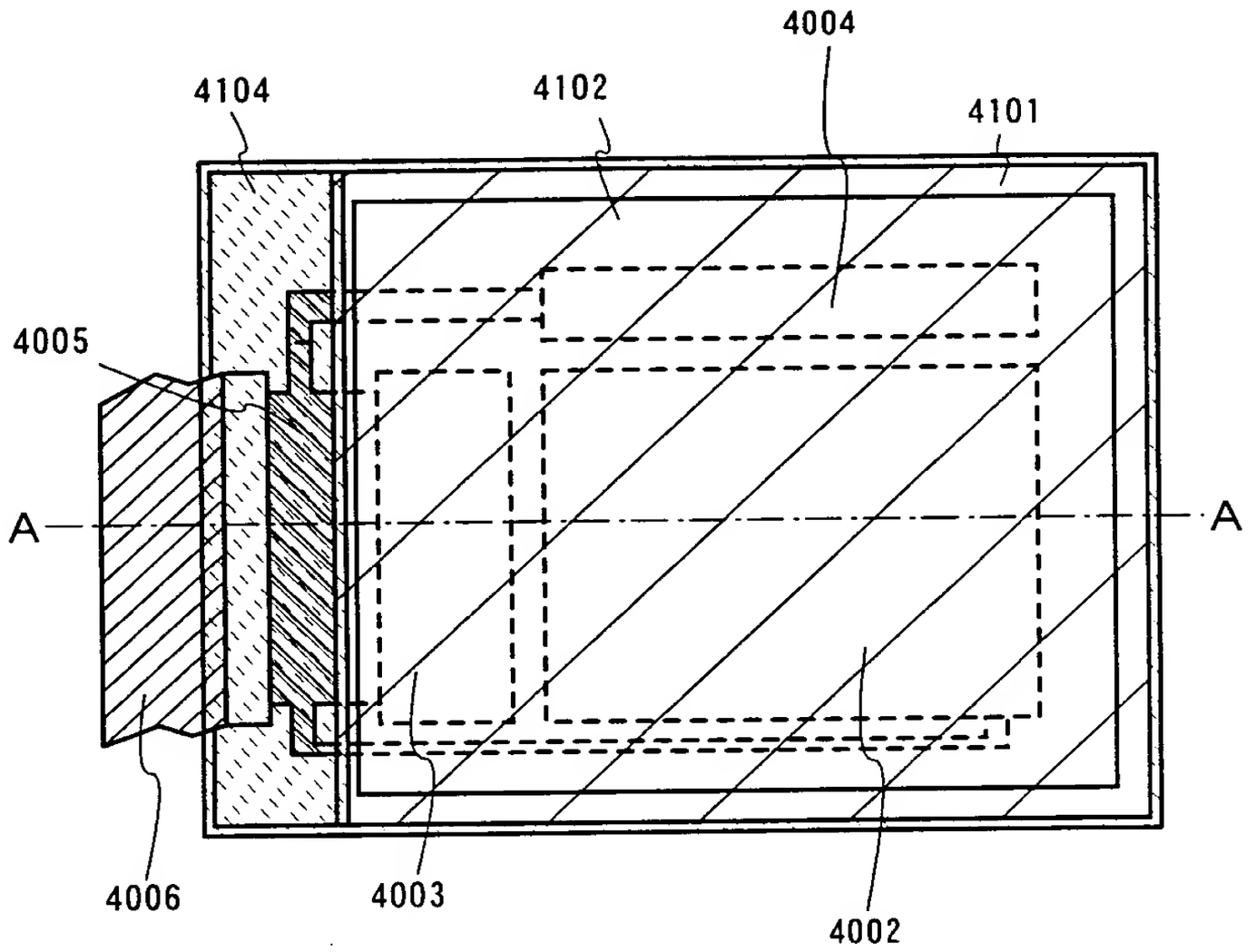
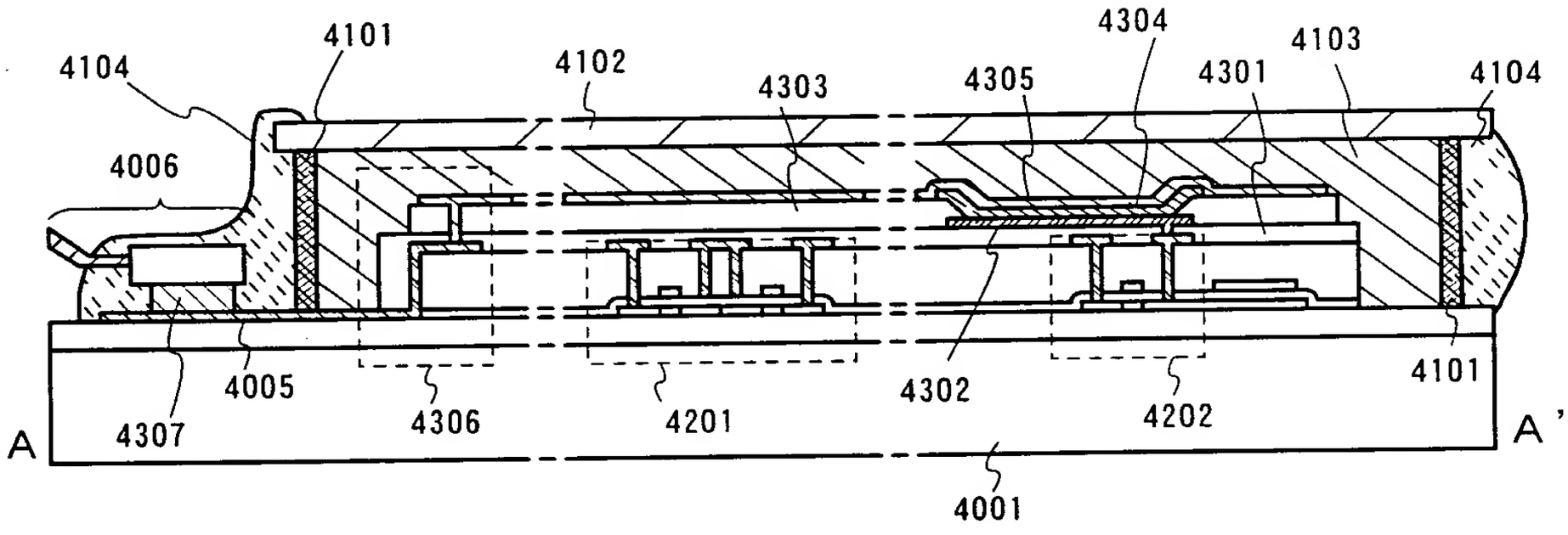


Fig.12



Fig,13


4104
4101
4102
4303
4305
4304
4301
4103
4104
4006
4005
4306
4201
4302
4202
4101
4307
A
A'
4001

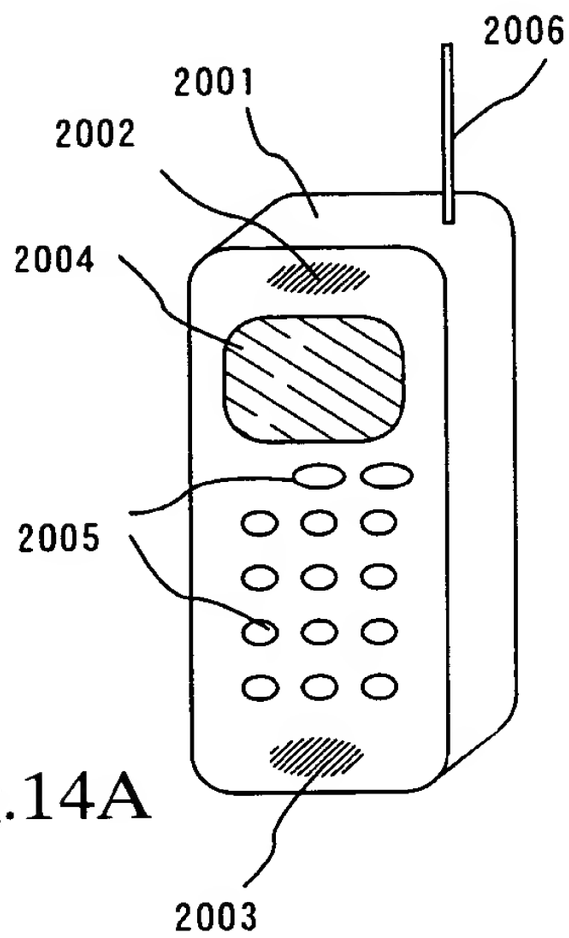


Fig.14A

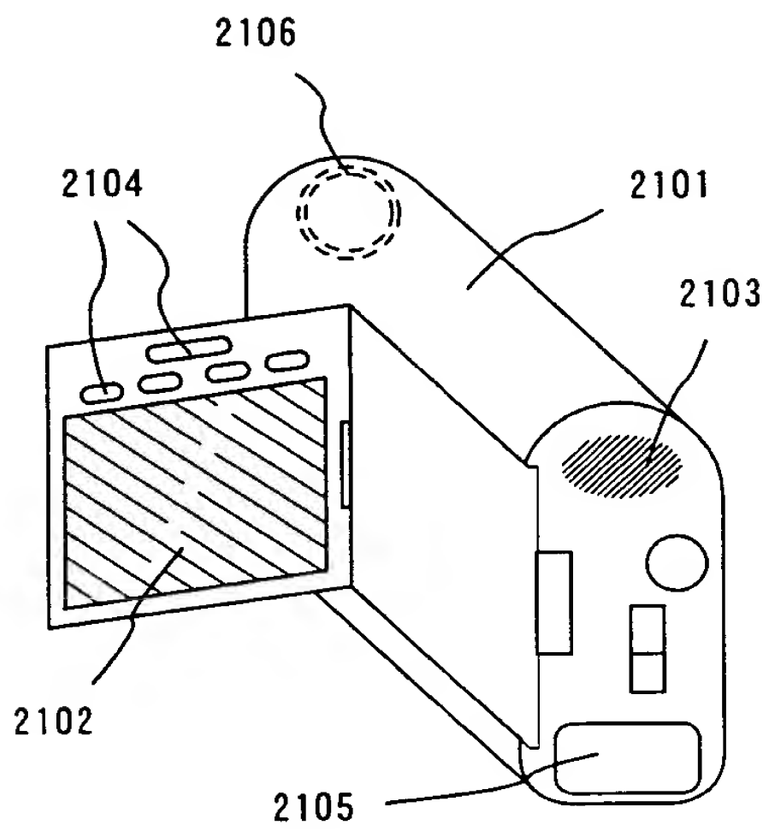


Fig.14B

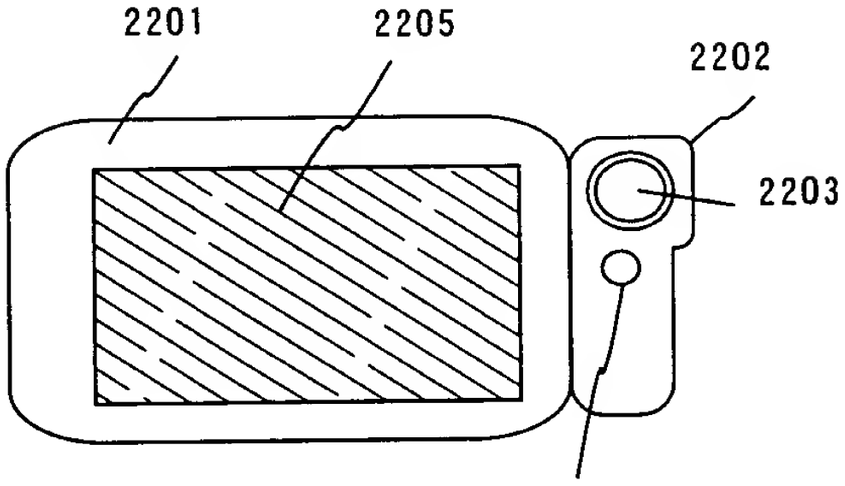


Fig.14C

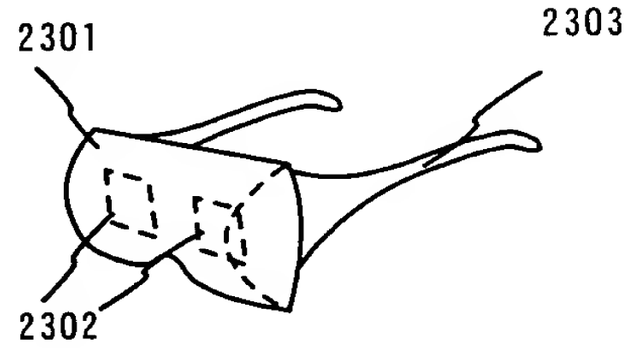


Fig.14D

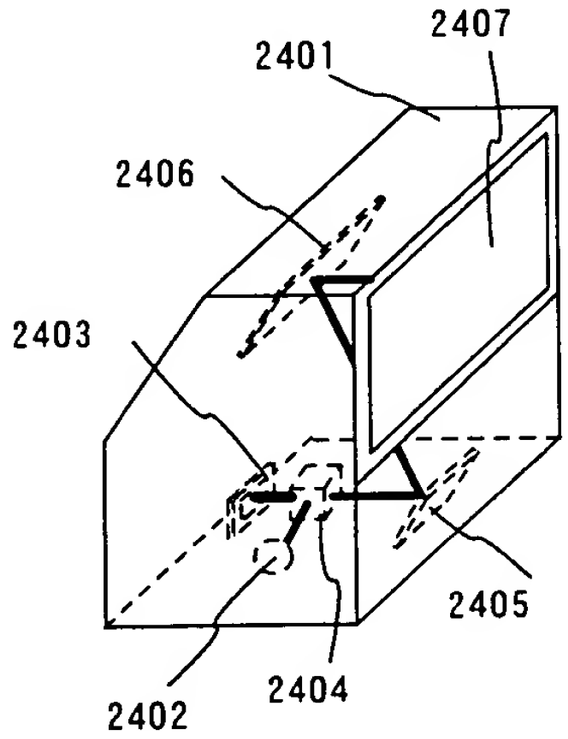


Fig.14E

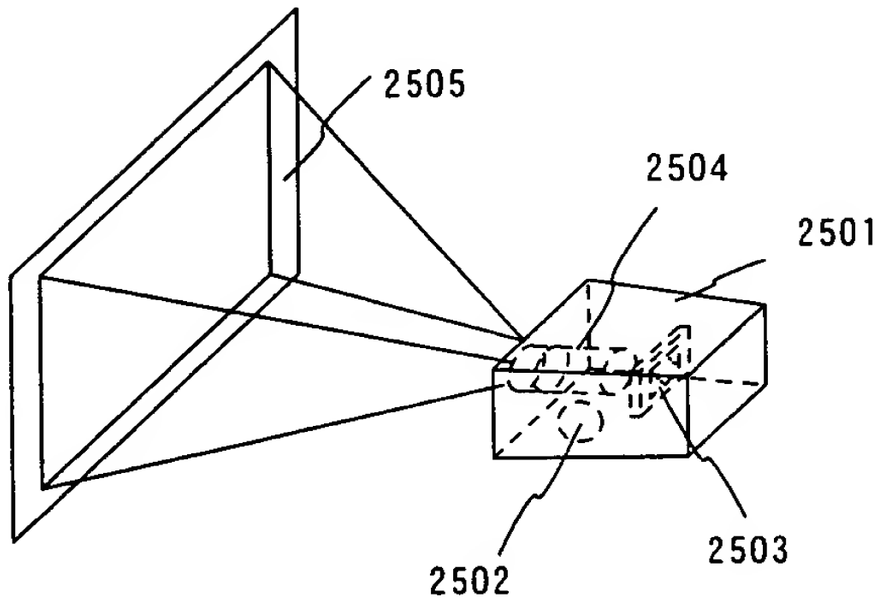


Fig.14F